# **Clock**Frequency**Generator**Sine

A low-footprint frequency generator which uses NetTimeLogic's clock IP core as source for synchronous frequency generation. Allows high-precision generation of any integer frequency with range O-10MHz and configurable polarity.

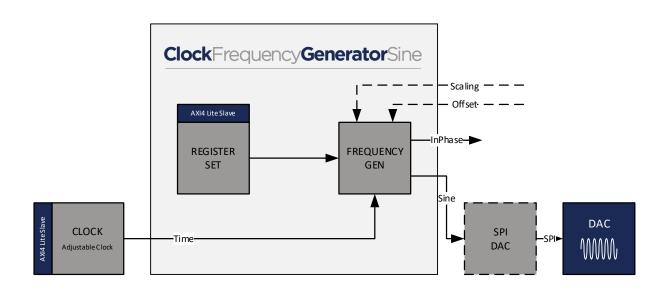
### Key Features:

- Clock aligned frequency generation as Sine Wave
- Configurable DAC Sample rate
- Auto phase realignment on time jumps
- Continuous generation
- Configurable polarity
- Output delay compensation
- AXI4 lite slave interface

## Typical Applications:

- Distributed data acquisition
- Test and measurement
- Robot control
- Substation automation
- Ethernet based automation networks
- Etc.

## IP Core Architecture:



Specification:

Generating Frequency aligned with NetTimeLogic's Clock (frequency and

phase at second crossing)

Output delay compensation takes also external delays like DAC,

SPI, RC into account

Configurable DAC Data width
Configurable DAC Sampling rate
Optional Scaling and DC Offset

Auto phase realignment on time jumps

Performance Output signal max frequency is ~200kHz at 2MSPS DAC sam-

pling

Portability Vendor independent, written in plain VHDL

Low footprint and low frequency requirements

Modularity and Simple time format can be also sourced by third-party clock core

scalability Slim and standardized interfaces are used

Configuration No CPU required, standalone configuration with signals

Axi4 lite slave support, for configuration

#### Deliverables:

IP core in plain VHDL

Testbench in plain VHDL

Reference Design

o Top level VHDL file

o Timing Constraint SDC files

o Vivado/Quartus Project file

Linux Driver

#### Related Products:

PTP Ordinary Clock

PTP Grandmaster Clock

PTP Hybrid Clock

PPS Master/Slave

IRIG Master/Slave

Adjustable Clock

• Signal Timestamper

Signal Generator



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