



FEATURES	Hive-S	Hive-M
PRICE	starting from 950 \$	starting from 1800 \$
POWER CONSUMPTION	2-3 W @ 5V	~15 W @ 24V
PASSIVE COOLING	✓	✓
BOOT-TIME	< 2 seconds	~1 minute
ARCHITECTURE	FPGA	SoC (System-on-Chip) / FPGA + CPU
CPU	-	Quad-Core ARM Cortex-A53
EXTENSION SLOTS	4x	4x
PERFORMANCE SLOTS	-	2x (RJ45 or SFP)
DEFAULT NETWORK INTERFACE	10/100 Mbit	10/100/1000 Mbit
OPTIONAL NETWORK INTERFACES	max. 4x 100 Mbit	max. 4x100 Mbit & 4x10/100/1000 Mbit
CONFIGURATION INTERFACE	Micro USB/UART	USB-C/UART and Ethernet
UNIVERSAL CONFIGURATION MANAGER (UCM)	✓	✓
UNIVERSAL WEB MANAGER (UWM)	-	✓
SYNCHRONIZATION OPTIONS (IN OR OUT)	GNSS, PTP, NTP, PPS, IRIG, DCF, CLOCK	
TIMESTAMPING ACCURACY	down to 1 ns	
SIGNAL GENERATION ACCURACY	down to 1 ns, up to 10 MHz	
HOLDOVER	< 10 us within 24h (after 10hrs locked)	
GPIOS	max. 32 IOs (3.3v) / 24 IOs (1.65-5.5V)	





IP CORE OPTIONS	Hive-S	Hive-M
PTP ORDINARY CLOCK	✓	✓
SYNCE	✓	✓
NTP CLIENT	✓	✓
NTP SERVER	✓	✓
PPS SLAVE	✓	✓
PPS MASTER	✓	✓
IRIG SLAVE	✓	✓
IRIG MASTER	✓	✓
TOD SLAVE	✓	✓
TOD MASTER	✓	✓
DCF SLAVE	✓	✓
DCF MASTER	✓	✓
SIGNAL TIMESTAMPER	✓	✓
SIGNAL GENERATOR	✓	✓
FREQUENCY GENERATOR	✓	✓
HSR/PRP	-	✓
PTP TRANSPARENT OR HYBRID CLOCK	-	✓
UNIVERSAL NET RED (UNR) / REDUNDANT GM	-	✓
TIME SENSITIVE NETWORKING (TSN)	TSN End-Node	TSN End-Node and Switched End-Node